

DATA SHEET

SMP1345 Series: Very Low Capacitance, Plastic Packaged Silicon PIN Diodes

Applications

- High isolation LNBs, WLANs, and wireless switches

Features

- Very low insertion loss: 0.4 dB
- Capacitance: 0.15 pF
- Packages rated MSL1, 260 °C per JEDEC J-STD-020



Skyworks Green™ products are compliant with all applicable legislation and are halogen-free. For additional information, refer to *Skyworks Definition of Green™*, document number SQ04-0074.





Description

The SMP1345 series of plastic packaged, surface mountable PIN diodes is designed for high volume Low-Noise Block (LNB), Wireless Local Area Network (WLAN), and switch applications from 10 MHz to 6 GHz. The short carrier lifetime of 100 ns (typical), combined with their thin I-region width of 10 μm (nominal) results in a group of fast speed RF switching PIN diodes.

The RF performance of the SMP1345 series is assured by virtue of their very low capacitance (0.15 pF) and low resistance (1.5 Ω at 10 mA).

Table 1 describes the various packages and marking of the SMP1345 series.

Table 1. SMP1345 Series Packaging and Marking

			
Common Anode	Common Cathode	Single	Single
SOT-23	SOT-23	SC-79 Green™	SOD-882 Green™
SMP1345-003LF Green™ Marking: RU9	SMP1345-004LF Green™ Marking: RU3	SMP1345-079LF Marking: Cathode and CF	SMP1345-040LF Marking: U
L _s = 1.5 nH	L _s = 1.5 nH	L _s = 0.7 nH	L _s = 0.45 nH



The Pb-free symbol or "LF" in the part number denotes a lead-free, RoHS-compliant package unless otherwise noted as Green™. Tin/lead (Sn/Pb) packaging is not recommended for new designs.

Electrical and Mechanical Specifications

The absolute maximum ratings of the SMP1345 series are provided in Table 2. Electrical specifications are provided in Table 3.

Typical performance characteristics of the SMP1345 series are illustrated in Figures 1 through 6.

Table 2. SMP1345 Series Absolute Maximum Ratings¹

Parameter	Symbol	Minimum	Maximum	Units
Reverse voltage	V_R		50	V
Power dissipation @ 25 °C lead temperature	P_D		250	mW
Storage temperature	T_{STG}	-65	+150	°C
Operating temperature	T_A	-65	+150	°C
Electrostatic discharge: Human Body Model (HBM), Class 1B	ESD		1000	V

¹ Exposure to maximum rating conditions for extended periods may reduce device reliability. There is no damage to device with only one parameter set at the limit and all other parameters set at or below their nominal value. Exceeding any of the limits listed here may result in permanent damage to the device.

ESD HANDLING: *Although this device is designed to be as robust as possible, electrostatic discharge (ESD) can damage this device. This device must be protected at all times from ESD when handling or transporting. Static charges may easily produce potentials of several kilovolts on the human body or equipment, which can discharge without detection. Industry-standard ESD handling precautions should be used at all times.*

Table 3. SMP1345 Series Electrical Specifications¹
($T_A = +25\text{ °C}$, Unless Otherwise Noted)

Parameter	Symbol	Test Condition	Min	Typical	Max	Units
Reverse current	I_R	$V_R = 50\text{ V}$			10	μA
Capacitance	C_T	$F = 1\text{ MHz}$: $V = 1\text{ V}$ $V = 5\text{ V}$		0.19 0.18	0.20	pF pF
Resistance	R_S	$F = 100\text{ MHz}$: $I = 1\text{ mA}$ $I = 10\text{ mA}$		3.5 1.5	2.0	Ω Ω
Forward voltage	V_F	$I_F = 10\text{ mA}$		0.89		V
Carrier lifetime	τ_I	$I_F = 10\text{ mA}$		100		ns
I region width				10		μm

¹ Performance is guaranteed only under the conditions listed in this table.

Typical Performance Characteristics

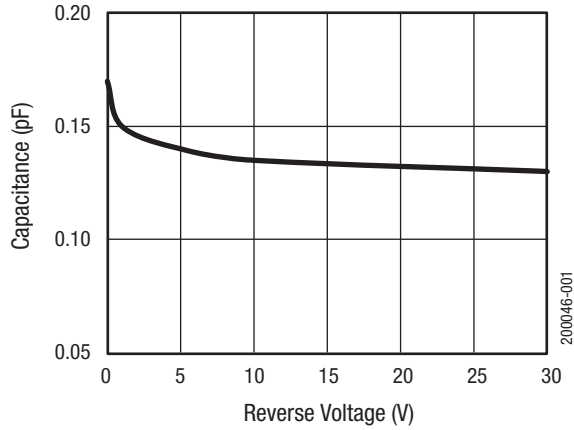


Figure 1. Total Capacitance vs Reverse Voltage

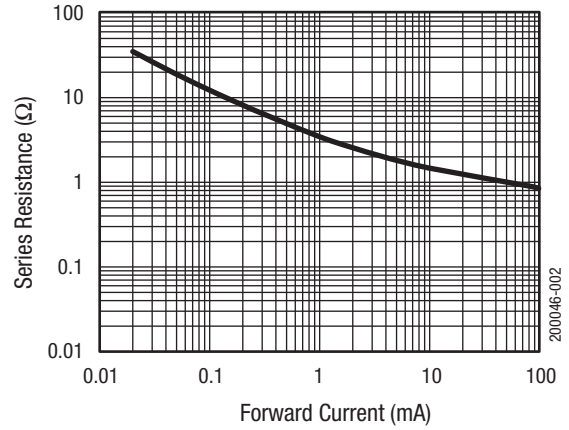


Figure 2. Series Resistance vs Current @ 100 MHz

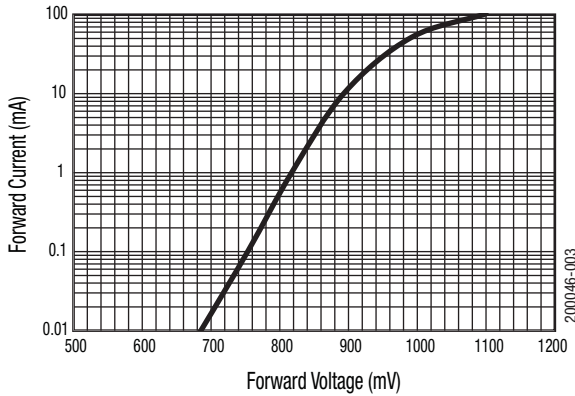
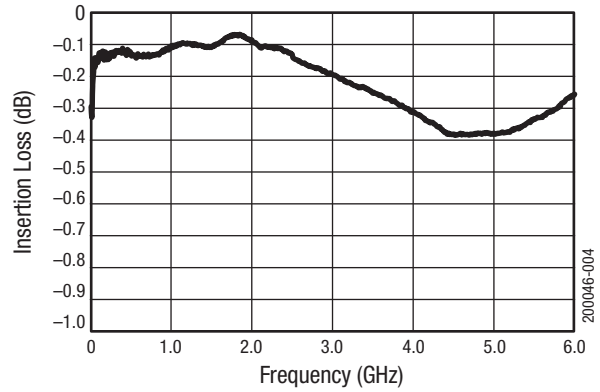
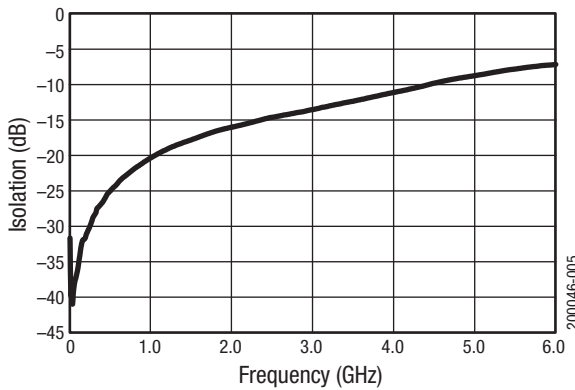


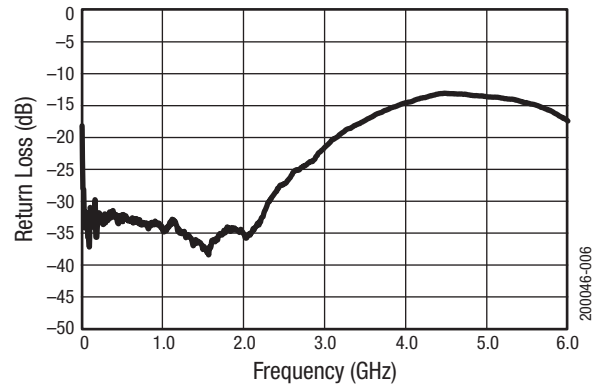
Figure 3. Forward Current vs Forward Voltage



**Figure 4. Insertion Loss vs Frequency
($I_F = 10$ mA)**



**Figure 5. Isolation vs Frequency
($V_R = 0$ V)**



**Figure 6. Return Loss vs Frequency
($I_F = 10$ mA)**

Package Dimensions

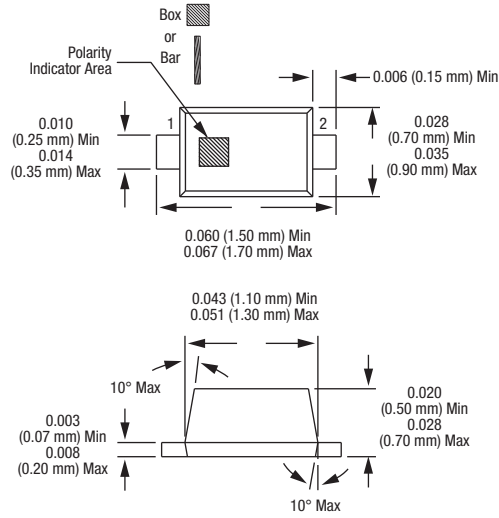
Package dimensions are shown in Figures 7 to 11 (odd numbers), and tape and reel dimensions are provided in Figures 8 to 12 (even numbers).

Package and Handling Information

Instructions on the shipping container label regarding exposure to moisture after the container seal is broken must be followed. Otherwise, problems related to moisture absorption may occur when the part is subjected to high temperature during solder assembly.

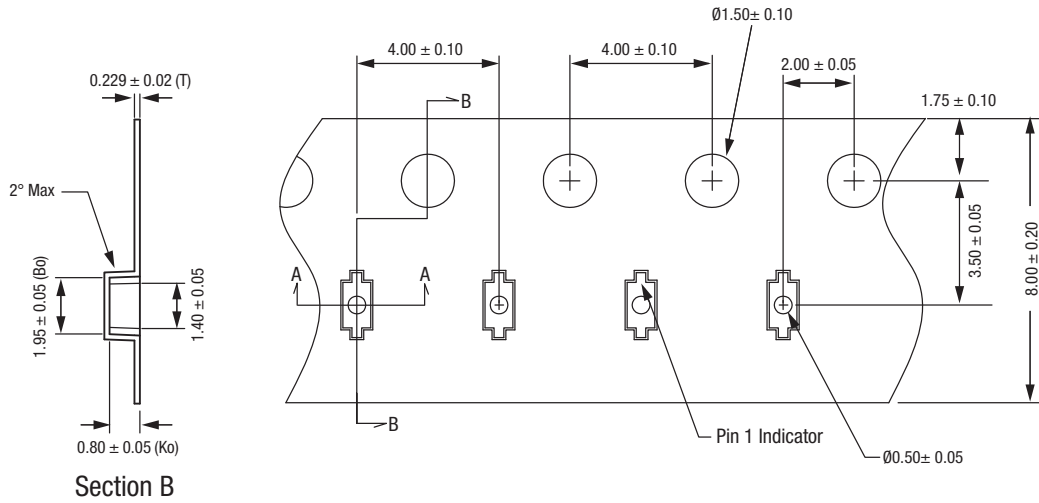
The SMP1345 series is rated to Moisture Sensitivity Level 1 (MSL1) at 260 °C. It can be used for lead or lead-free soldering. For additional information, refer to the Skyworks Application Note, *Solder Reflow Information*, document number 200164.

Care must be taken when attaching this product, whether it is done manually or in a production solder reflow environment. Production quantities of this product are shipped in a standard tape and reel format.



Dimensions are in inches (millimeters shown in parentheses) 200046-007

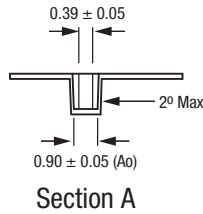
Figure 7. SC-79 Package Dimension Drawing



Section B

Notes:

1. Carrier tape: black conductive polycarbonate or polystyrene.
2. Cover tape material: transparent conductive PSA.
3. Cover tape size: 5.4 mm width.
4. ESD-surface resistivity is $\leq 1 \times 10^8$ Ohms/square per EIA, JEDEC TNR Specification.
5. All measurements are in millimeters.



Section A

200046-008

Figure 8. SC-79 Tape and Reel Dimensions

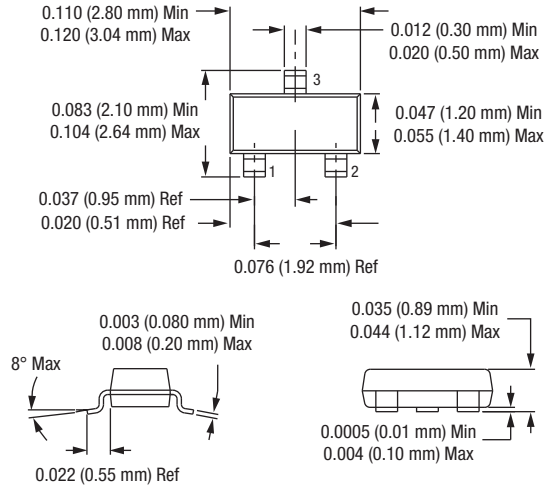
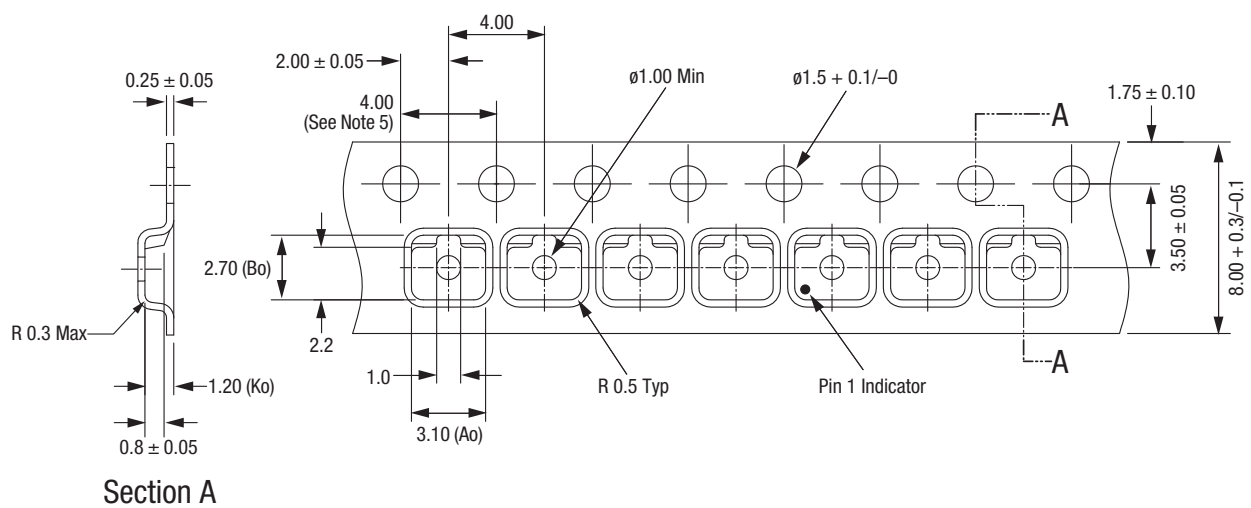
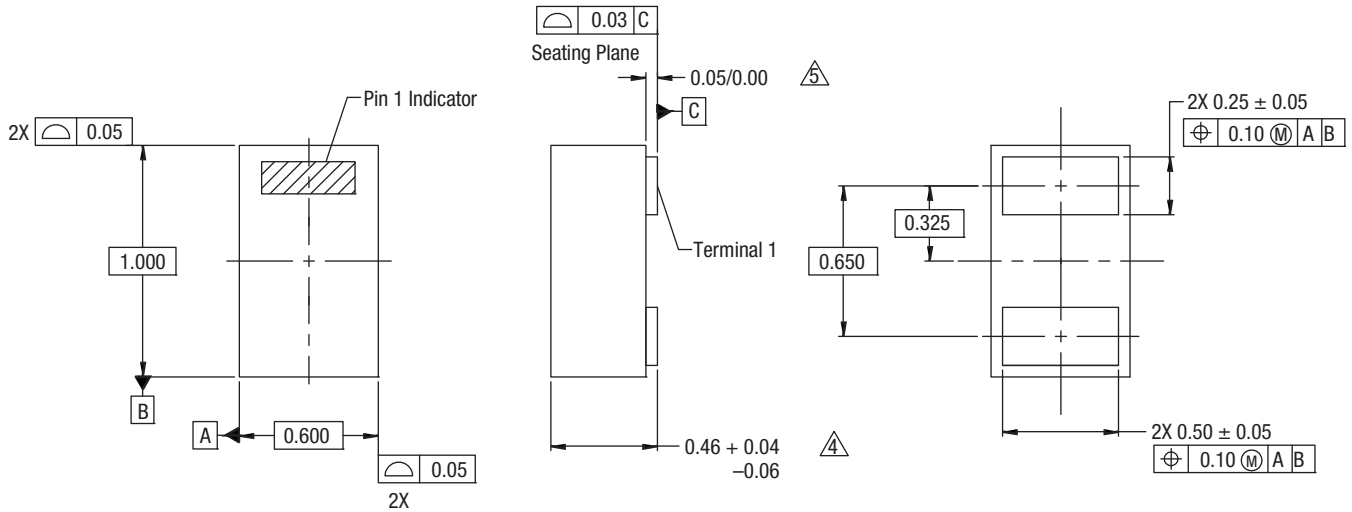


Figure 9. SOT-23 Package Dimension Drawing



- Notes:
1. Carrier tape: black conductive polycarbonate.
 2. Cover tape material: transparent conductive PSA.
 3. Cover tape size: 5.40 mm width.
 4. Tolerance ±0.10 mm.
 5. Ten sprocket hole pitch cumulative tolerance: ±0.2 mm.
 6. All measurements are in millimeters.
 7. Alternative carrier tape dimensions are:
 Ao = 3.3
 Bo = 2.9
 Ko = 1.22

Figure 10. SOT-23 Tape and Reel Dimensions

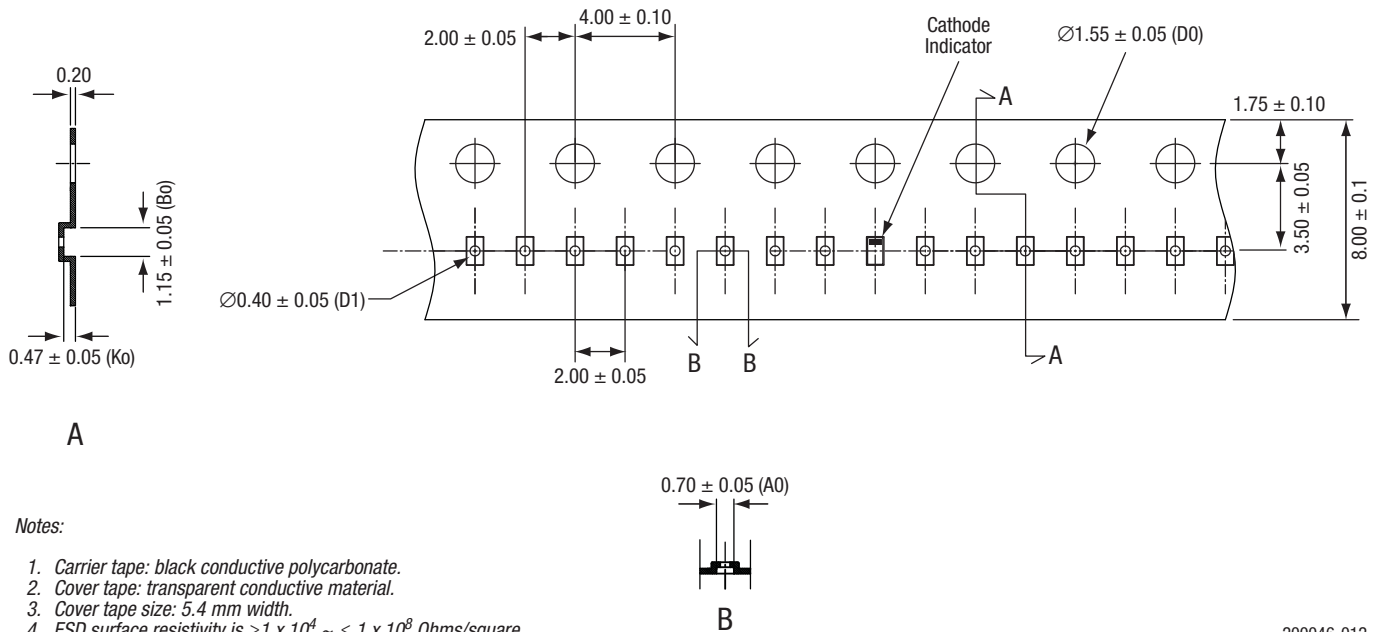


Notes:

1. All measurements are in millimeters.
2. Dimensions and tolerances according to ASME Y14.5M-1994.
3. These packages are used principally for discrete devices.
4. This dimension includes stand-off height and package body thickness, but does not include attached features, e.g., external heatsink or chip capacitors. An integral heatslug is not considered an attached feature.
5. This dimension is primarily terminal plating, but does not include small metal protrusion.

200046-011

Figure 11. SOD-882 Package Dimension Drawing



Notes:

1. Carrier tape: black conductive polycarbonate.
2. Cover tape: transparent conductive material.
3. Cover tape size: 5.4 mm width.
4. ESD surface resistivity is $\geq 1 \times 10^4 \sim \leq 1 \times 10^8$ Ohms/square.
5. All dimensions are in millimeters.

200046-012

Figure 12. SOD-882 Tape and Reel Drawing

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