



# SAK-TC237LP-32F200N AC

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## Parameters

Parameters	SAK-TC237LP-32F200N AC
A/D Input Lines	24
A/D Input Lines (incl. FADC)	24
CAN Nodes	6
Clock Frequency <b>max</b>	200.0 MHz
DMA Channels	16
DSP Functionality	yes

Parametrics	SAK-TC237LP-32F200N AC
External Bus Interface	no
Fast Flash Programming	no
FlexRay™	yes
Floating Point Unit	yes
I/O Operation Voltages	3.3 V
Instruction Set Architecture	TriCore™ v1.6E (32-bit)
Instruction Width ([bits])	32/16
Number of ADC Modules	2.0
On-chip Clock Generation	yes
On-chip Voltage Regulator	yes
Oscillator Watchdog	yes
Program Memory	2.0 MByte
Real Time Clock	yes
SRAM (incl. Cache)	192.0 kByte

Parametrics	SAK-TC237LP-32F200N AC
Temperature	-40°C - +125 °C
Touch/LED Matrix Control	no
Type of Memory	Flash
Watchdog Timer	yes
Parametrics	SAK-TC237LP-32F200N AC

## Documents

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### **Application Notes**

- [An introduction to MTV Multi-Core Debug Solution \(MCDS\) Trace ViewerEN](#)

01\_00 | 2018-04-03 | pdf | 1.1 MB

- [EMC and System-ESD Design Guidelines for Board LayoutEN](#)

01\_00 | 2018-03-15 | pdf | 3 MB

### **Material Content Sheet Info**

- [MCDS 2018-01-23 09-00-30 MA001654636 PG-LFBGA-292-6EN](#)

01\_00 | 2018-02-05 | pdf | 28 KB

### **Product Selection Guide**

- [Power and Sensing Selection Guide 2018EN](#)

00\_00 | 2018-02-19 | pdf | 23.9 MB

## [Presentations](#)

- [Microcontroller Pocket GuideEN](#)

01\_00 | 2018-02-22 | pdf | 2.6 MB

## Order

<b>Sales Product Name</b>	SAK-TC237LP-32F200N AC
<b>OPN</b>	TC237LP32F200NACKXUMA1
<b>Product Status</b>	active and preferred
<b>Package name</b>	<a href="#">PG-LFBGA-292</a>
<b>Completely lead free</b>	yes
<b>Halogen free</b>	yes
<b>RoHS compliant</b>	yes
<b>Packing Size</b>	1000
<b>Packing Type</b>	TAPE & REEL
<b>Moisture Level</b>	
<b>Moisture Packing</b>	DRY