



Xilinx

Virtex-5 OpenSPARC FPGA Development Board : ML509 (RETIRED) SKU: 6003-410-008-RET



Product Description

The Virtex[®]-5 OpenSPARC Evaluation Platform is a powerful system for hosting the OpenSPARC T1 open-source microprocessor. Equivalent to the Xilinx[®] ML509 board and based on the Xilinx XUPV5-LX110T FPGA, this kit brings the throughput of OpenSPARC Chip Multi-Threading to an FPGA.

OpenSPARC T1 is the open-sourced version of the custom designed UltraSPARC T1 microprocessor from Sun Microsystems. To broaden the appeal of this state-of-the-art Chip Multi-threading (CMT) technology to the developers, engineers at Sun Microsystems and Xilinx Inc. have developed a reference design that allows a scaled-down version of the OpenSPARC T1 processor to run on Xilinx Virtex-5 FPGAs. This reference design is an excellent starting point for researchers and entrepreneurs to build and test novel ideas in the areas of computer architecture, logic design, parallel programming, and compiler techniques, among others. For more details on how to download the OpenSPARC design and the reference design, please visit www.opensparc.net/fpga.

Kit Includes:

- XUPV5-LX110T board
- 1GB Compact Flash card
- 256 MB SODIMM module
- SATA cable
- XUP USB-JTAG Programming Cable
- DVI to VGA adapter
- 4A power supply

Note:

For documentation and reference designs, please visit <http://www.xilinx.com/univ/xupv5-lx110t.htm>

For more information and to learn more about bringing Sun's OpenSPARC program to your university, please visit www.opensparc.net/edu/university-program.html. For reference projects, design resources, and the most up-to-date documentation, please visit the Virtex-5 OpenSPARC Evaluation Platform Resource Center.

Stats:

Processor/IC: Xilinx Virtex-5 XC5VLX110T FPGA

Connector(s):

- USB (2) – Host and Peripheral
- PS/2 (2) – Keyboard, Mouse
- RJ-45 – 10/100/1000 Networking
- RS-232 (Male) – Serial port
- Audio In (2) – Line, Microphone
- Audio Out (2) – Line, Amp, SPDIF
- Video Input
- Video (DVI/VGA) Output
- Single-Ended and Differential I/O Expansion

Programming: JTAG Programming Interface

Features:

- Xilinx Virtex-5 XC5VLX110T FPGA
- Two Xilinx XCF32P Platform Flash PROMs (32 Mbyte each) for storing large device configurations
- Xilinx SystemACE Compact Flash configuration controller
- 64-bit wide 256Mbyte DDR2 small outline DIMM (SODIMM) module compatible with EDK supported IP and software drivers
- On-board 32-bit ZBT synchronous SRAM and Intel® P30 StrataFlash™
- 10/100/1000 tri-speed Ethernet PHY supporting MII, GMII, RGMII, and SGMII interfaces
- USB host and peripheral controllers
- Programmable system clock generator
- Stereo AC97 codec with line in, line out, headphone, microphone, and SPDIF digital audio jacks
- RS-232 port, 16x2 character LCD, and many other I/O devices and ports